		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		10/001,478	NEMECEK ET AL.
		Examiner	Art Unit	Page 1 of 1
		Jason Proctor	2123	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,202,044	03-2001	Tzori, Yiftach	703/28
	B	US-5,748,875	05-1998	Tzori, Yifatch	714/29
	C	US-5,978,584	11-1999	Nishibata et al.	717/134
	D	US-6,356,862	03-2002	Bailey, Brian	703/16
	E	US-6,718,294	04-2004	Bortfeld, Ulrich	703/20
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Matthew Dahl, Jonathan Babb, Russel Tessier, Silvina Hanono, David Hoki, and Anant Agarwal; Emulation of the Sparcle Microprocessor with the MIT Virtual Wires Emulation System; 1994; IEEE; pages 14-22
	V	Jerry Bauer, Michael Bershteyn, Ian.Kaplan, Paul Vyedin; A Reconfigurable Logic Machine for Fast Event-Driven Simulation; 1998; Design Automation Conference Proceedings; pages 668-671
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

U.S. Patent and Trademark Office
PTO-892 (Rev. 01-2001)   **Notice of References Cited**   Part of Paper No. 20050506